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☐ 3. Document ID: US 20030167307 A1

L10: Entry 3 of 30

File: PGPB

Sep 4, 2003

PGPUB-DOCUMENT-NUMBER: 20030167307

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20030167307 A1

TITLE: Interactive computer network and method of operation

PUBLICATION-DATE: September 4, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Filepp, Robert	Springfield	NJ	US	
Gordon, Michael L.	Dobbs Ferry	NY	US	
Bidwell, Alexander W.	New York	NY	US	
Young, Francis C.	Pearl River	NY	US	
Wolf, Allan M.	Ridgefield	CT	US	
Meo, Sam	New York	NY	US	
Tiemann, Duane	Ossining	NY	US	
Abrahams, Lawrence	Hastings-on-Hudson	NY	US	
Silfen, Michael J.	Croton-on-Hudson	NY	US	
Dalsass, Aldo R.	Oakland	NJ	US	
Lee, Florence M.	Stamford	CT	US	
Appleman, Kenneth H.	White Plains	NY	US	

US-CL-CURRENT: 709/205

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
Draw Desc	Image								

KMIC

☐ 4. Document ID: US 20030154403 A1

L10: Entry 4 of 30

File: PGPB

Aug 14, 2003

PGPUB-DOCUMENT-NUMBER: 20030154403

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20030154403 A1

TITLE: Web-based security with controlled access to data and resources

PUBLICATION-DATE: August 14, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Keinsley, Brian E.	Corydon	IN	US	
Edwards, Brett T.	Louisville	KY	US	
Rosenberg, Sidddy	Louisville	KY	US	
Light, Eric P.	Louisville	KY	US	
Townsend, David L.	Louisville	KY	US	
Smithson, Mark A.	Louisville	KY	US	
Harris, Sharon A.	Villanova	PA	US	
Lawhead, Aaron L.	New Albany	IN	US	
Stanley, Craig	Jeffersonville	IN	US	
Weber, Leigh S.	Maple Glen	PA	US	
Latimer, Eleanor W.	Dallas	TX	US	
Burchard, William	Louisville	KY	US	

US-CL-CURRENT: 713/201; 709/223

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
Draw Desc	Image								

KWIC

☐ 5. Document ID: US 20030061623 A1

L10: Entry 5 of 30

File: PGPB

Mar 27, 2003

PGPUB-DOCUMENT-NUMBER: 20030061623

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20030061623 A1

TITLE: Highly integrated media access control

PUBLICATION-DATE: March 27, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Denney, Lisa Voigt	Suwanee	GA	US	
Moshar, Hooman	Fountain Valley	CA	US	
Horton, John Daniel JR.	Alpharetta	GA	US	
Lansing, Shane Patrick	Mission Viejo	CA	US	
Nazareth, Sean Francis	Irvine	CA	US	
Pantelias, Niki Roberta	Duluth	GA	US	

US-CL-CURRENT: 725/125; 348/192, 714/746

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
Draw Desc	Image								

KWIC

☐ 6. Document ID: US 20020198855 A1

L10: Entry 6 of 30

File: PGPB

Dec 26, 2002

PGPUB-DOCUMENT-NUMBER: 20020198855

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20020198855 A1

TITLE: Collection content classifier

PUBLICATION-DATE: December 26, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Jameson, Kevin Wade	Calgary		CA	

US-CL-CURRENT: 706/45

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	KWIC
Draw Desc	Image									

☐ 7. Document ID: US 20020103992 A1

L10: Entry 7 of 30

File: PGPB

Aug 1, 2002

PGPUB-DOCUMENT-NUMBER: 20020103992

PGPUB-FILING-TYPE: new

DOCUMENT-IDENTIFIER: US 20020103992 A1

TITLE: In-line code suppression

PUBLICATION-DATE: August 1, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Perry, Patrick E.	Shelburne	VT	US	
Ventrone, Sebastian T.	South Burlington	VT	US	

US-CL-CURRENT: 712/229; 712/227, 713/320

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	KWIC
Draw Desc	Image									

☐ 8. Document ID: US 20010007128 A1

L10: Entry 8 of 30

File: PGPB

Jul 5, 2001

PGPUB-DOCUMENT-NUMBER: 20010007128

PGPUB-FILING-TYPE: new-utility

DOCUMENT-IDENTIFIER: US 20010007128 A1

TITLE: Security mechanism providing access control for locally-held data

PUBLICATION-DATE: July 5, 2001

INVENTOR-INFORMATION:

NAME	CITY	STATE	COUNTRY	RULE-47
Lambert, Howard Shelton	Hedge End		GB	
Orchard, James Ronald	Winchester		GB	

US-CL-CURRENT: 713/165; 713/201

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
Draw Desc	Image								

K/MC

☐ 9. Document ID: US 6556875 B1

L10: Entry 9 of 30

File: USPT

Apr 29, 2003

US-PAT-NO: 6556875

DOCUMENT-IDENTIFIER: US 6556875 B1

TITLE: Device control system

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
Draw Desc	Image								

K/MC

☐ 10. Document ID: US 6367017 B1

L10: Entry 10 of 30

File: USPT

Apr 2, 2002

US-PAT-NO: 6367017

DOCUMENT-IDENTIFIER: US 6367017 B1

TITLE: Apparatus and method for providing and authentication system

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
Draw Desc	Image								

K/MC

☐ 11. Document ID: US 6275852 B1

L10: Entry 11 of 30

File: USPT

Aug 14, 2001

US-PAT-NO: 6275852

DOCUMENT-IDENTIFIER: US 6275852 B1

TITLE: Interactive computer network and method of operation

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
Draw Desc	Image								

K/MC

☐ 12. Document ID: US 6199100 B1

L10: Entry 12 of 30

File: USPT

Mar 6, 2001

US-PAT-NO: 6199100

DOCUMENT-IDENTIFIER: US 6199100 B1

TITLE: Interactive computer network and method of operation

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
Draw Desc	Image								

K/MC

☐ 13. Document ID: US 6198487 B1

L10: Entry 13 of 30

File: USPT

Mar 6, 2001

US-PAT-NO: 6198487

DOCUMENT-IDENTIFIER: US 6198487 B1

TITLE: Ole for design and modeling

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
Draw Desc	Image								

K00C

☐ 14. Document ID: US 6115711 A

L10: Entry 14 of 30

File: USPT

Sep 5, 2000

US-PAT-NO: 6115711

DOCUMENT-IDENTIFIER: US 6115711 A

TITLE: Method and apparatus for generating transactions and a dialog flow manager

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
Draw Desc	Image								

K00C

☐ 15. Document ID: US 6115710 A

L10: Entry 15 of 30

File: USPT

Sep 5, 2000

US-PAT-NO: 6115710

DOCUMENT-IDENTIFIER: US 6115710 A

**** See image for Certificate of Correction ****TITLE: Portable and dynamic distributed transaction management method

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
Draw Desc	Image								

K00C

☐ 16. Document ID: US 6085265 A

L10: Entry 16 of 30

File: USPT

Jul 4, 2000

US-PAT-NO: 6085265

DOCUMENT-IDENTIFIER: US 6085265 A

TITLE: System for handling an asynchronous interrupt a universal serial bus device

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
Draw Desc	Image								

K00C

☐ 17. Document ID: US 5960004 A

L10: Entry 17 of 30

File: USPT

Sep 28, 1999

US-PAT-NO: 5960004

DOCUMENT-IDENTIFIER: US 5960004 A

TITLE: Modular application software for telecommunications exchanges for providing all end user services traffic handling and charging requirements of an applications type

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
Draw Desc	Image								

KMIC

☐ 18. Document ID: US 5910804 A

L10: Entry 18 of 30

File: USPT

Jun 8, 1999

US-PAT-NO: 5910804

DOCUMENT-IDENTIFIER: US 5910804 A

TITLE: OLE for design and modeling

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
Draw Desc	Image								

KMIC

☐ 19. Document ID: US 5896530 A

L10: Entry 19 of 30

File: USPT

Apr 20, 1999

US-PAT-NO: 5896530

DOCUMENT-IDENTIFIER: US 5896530 A

TITLE: Portable and dynamic distributed applications architecture

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
Draw Desc	Image								

KMIC

☐ 20. Document ID: US 5819089 A

L10: Entry 20 of 30

File: USPT

Oct 6, 1998

US-PAT-NO: 5819089

DOCUMENT-IDENTIFIER: US 5819089 A

TITLE: Portable and dynamic distributed transaction management method

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
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Term	Documents
(19 NOT 20).USPT,PGPB,JPAB,EPAB,DWPI,TDBD.	8
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(field or bit\$1) near5 execut\$3 near6
(next or subsequent\$3 or current\$2 or
preced\$3) near9 (branch\$3 or jump\$4)

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result set

DB=USPT,PGPB,JPAB,EPAB,DWPI,TDBD; PLUR=YES; OP=OR

<u>L21</u>	l19 not L20	8	<u>L21</u>
<u>L20</u>	L18 near7 (field\$1 or bit\$1)	90	<u>L20</u>
<u>L19</u>	L18 near10 (field\$1 or bit\$1)	98	<u>L19</u>
<u>L18</u>	(bypass\$3 or skip\$4) near15 (unnecessary or unused or redundan\$4)	2492	<u>L18</u>
<u>L17</u>	(field or bit\$1) near5 execut\$3 near6 (next or subsequent\$3 or current\$2 or preceed\$3) and (bypass\$3 or skip\$4) near15 (unnecessary or unused or redundan\$4)	6	<u>L17</u>
<u>L16</u>	L10 and (unnecessary or unused or redundan\$4)	336	<u>L16</u>
<u>L15</u>	(field or bit\$1) near5 execut\$3 near6 (next or subsequent\$3 or current\$2 or preceed\$3) near100 (bypass\$3 or skip\$4)	13	<u>L15</u>
<u>L14</u>	(field or bit\$1) near5 execut\$3 near6 (next or subsequent\$3 or current\$2 or preceed\$3) near60 (bypass\$3 or skip\$4)	13	<u>L14</u>
<u>L13</u>	L12 not l11	1	<u>L13</u>
<u>L12</u>	(field or bit\$1) near5 execut\$3 near6 (next or subsequent\$3 or current\$2 or preceed\$3) near30 (bypass\$3 or skip\$4)	13	<u>L12</u>
<u>L11</u>	(field or bit\$1) near5 execut\$3 near6 (next or subsequent\$3 or current\$2 or preceed\$3) near15 (bypass\$3 or skip\$4)	12	<u>L11</u>
<u>L10</u>	(field or bit\$1) near5 execut\$3 near6 (next or subsequent\$3 or current\$2 or preceed\$3) and (bypass\$3 or skip\$4)	559	<u>L10</u>
<u>L9</u>	(field or bit\$1) near5 execut\$3 near6 (next or subsequent\$3 or current\$2 or preceed\$3)	1947	<u>L9</u>
<u>L8</u>	(field or bit\$1) near5 execut43 near6 (next or subsequent\$3 or current\$2 or preceed\$3)	0	<u>L8</u>

DB=USPT; PLUR=YES; OP=OR

<u>L7</u>	((717/)!.CCLS.)	0	<u>L7</u>
<u>L6</u>	((717/)!.CCLS.)	0	<u>L6</u>

DB=USPT,PGPB,JPAB,EPAB,DWPI,TDBD; PLUR=YES; OP=OR

<u>L5</u>	l2 and skip\$5	24	<u>L5</u>
<u>L4</u>	L2 and bypass\$3	24	<u>L4</u>
<u>L3</u>	L2 and bypass43	0	<u>L3</u>
<u>L2</u>	(field\$1 or bit\$1) near5 instruction near8 (skip\$5 or bypass\$3 or jump\$5 or branch\$3) near9 format	105	<u>L2</u>
<u>L1</u>	(field\$1 or bit\$1) near5 instruction near8 (skip\$5 or bypass\$3 or jump\$5 or branch\$3)	3167	<u>L1</u>

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Database:

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L39

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DATE: Wednesday, December 03, 2003 [Printable Copy](#) [Create Case](#)
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result set

DB=USPT,PGPB,JPAB,EPAB,DWPI,TDBD; PLUR=YES; OP=OR

<u>L39</u>	L38 not l34	69	<u>L39</u>
<u>L38</u>	(unnecessary or redundan\$4) near5 (software or application\$1 or program\$1) near8 (state\$1 or status) and (reduc\$3 or sav\$3 or conserv\$6) near4 (time or power or resource\$3)	72	<u>L38</u>
<u>L37</u>	(unnecessary or redundan\$4) near5 (software or application\$1 or program\$1) near8 (state\$1 or status)	236	<u>L37</u>
<u>L36</u>	(unnecessary or redundan\$4) near9 (software or application\$1 or program\$1) near8 (state\$1 or status)	414	<u>L36</u>
<u>L35</u>	L34 not l26	5	<u>L35</u>
<u>L34</u>	L33 and (reduc\$3 or sav\$3 or conserv\$6) near4 (time or power or resource\$3)	51	<u>L34</u>

<u>L33</u>	(disabl\$3 or de near1 activat\$3 or supress\$6 or bypass\$3 or skip\$4 or select\$6) near9 (unnecessary or redundan\$4) near6 (application\$1 or software)	251	<u>L33</u>
<i>DB=USPT; PLUR=YES; OP=OR</i>			
<u>L32</u>	5117449.pn.	1	<u>L32</u>
<u>L31</u>	5495484.pn.	1	<u>L31</u>
<u>L30</u>	5511067.pn.	1	<u>L30</u>
<u>L29</u>	5541976.pn.	1	<u>L29</u>
<i>DB=USPT,PGPB,JPAB,EPAB,DWPI,TDBD; PLUR=YES; OP=OR</i>			
<u>L28</u>	6097955.pn.	2	<u>L28</u>
<u>L27</u>	L26 not l23	45	<u>L27</u>
<u>L26</u>	(supress\$6 or bypass\$3 or skip\$4 or select\$6) near9 (unnecessary or redundan\$4) near6 (application\$1 or software) and (reduc\$3 or sav\$3) near4 (time or power)	46	<u>L26</u>
<u>L25</u>	(supress\$6 or bypass\$3 or skip\$4 or select\$6) near9 (unnecessary or redundan\$4) near6 (application\$1 or software) and (reduc\$3 or sav\$3)	153	<u>L25</u>
<u>L24</u>	(supress\$6 or bypass\$3 or skip\$4 or select\$6) near9 (unnecessary or redundan\$4) near6 (application\$1 or software)	247	<u>L24</u>
<u>L23</u>	(supress\$6 or bypass\$3 or skip\$4) near9 (unnecessary or redundan\$4) near6 (application\$1 or software)	21	<u>L23</u>
<u>L22</u>	l17 and reduc\$3 near4 power	241	<u>L22</u>
<u>L21</u>	l17 and reduc\$3	2641	<u>L21</u>
<u>L20</u>	(supress\$6 or bypass\$3 or skip\$4 or disabl\$3 or de near1 activat\$3) near5 (code\$1 or component\$1 or block\$1) near5 (application or software) near5 select\$6	31	<u>L20</u>
<u>L19</u>	L17 near15 power near4 sav\$3	0	<u>L19</u>
<u>L18</u>	L17 and power near4 sav\$3	63	<u>L18</u>
<u>L17</u>	(supress\$6 or bypass\$3 or skip\$4 or select\$6) near5 (code\$1 or component\$1) near5 (application or software)	4342	<u>L17</u>
<u>L16</u>	l14 and sav\$3 near5 power\$1	0	<u>L16</u>
<u>L15</u>	(supress\$6 or bypass\$3 or skip\$4) near5 (code\$1 or component\$1) near5 (application or software) near6 execut\$4	6	<u>L15</u>
<u>L14</u>	(supress\$6 or bypass\$3 or skip\$4) near5 (code\$1 or component\$1) near5 (application or software)	137	<u>L14</u>
<u>L13</u>	L12 not l10	32	<u>L13</u>
<u>L12</u>	l9 and activat\$3	57	<u>L12</u>
<u>L11</u>	L9 and (skip\$4 or bypass\$3 or supress\$6) and activat\$3	25	<u>L11</u>
<u>L10</u>	L9 and (skip\$4 or bypass\$3 or supress\$6)	30	<u>L10</u>
<u>L9</u>	L8 and mode\$1	81	<u>L9</u>
<u>L8</u>	L7 and (palm\$3 or pager\$1 or personal or handheld or portable)	85	<u>L8</u>

<u>L7</u>	(supress\$3 or disabl\$3 or de near2 activat\$3 or activat\$3 or enabl\$3 or bypass\$3 or skip\$4) near5 (code\$1 or segment\$1 or routine\$1 or function\$1 or program\$1 or instruction\$1) near6 support\$3 near5 application\$1	154	<u>L7</u>
<u>L6</u>	(supress\$3 or disabl\$3 or de near2 activat\$3 or activat\$3 or enabl\$3 or bypass\$3 or skip\$4) near5 (code\$1 or segment\$1 or routine\$1 or function\$1 or program\$1 or instruction\$1) near6 support\$3	2298	<u>L6</u>
<u>L5</u>	L4 and (palm\$3 or pager\$1 or personal or handheld or portable)	226	<u>L5</u>
<u>L4</u>	L1 and support\$3 near5 application and power near4 sav\$3	255	<u>L4</u>
<u>L3</u>	L1 and support\$3 near5 application	7349	<u>L3</u>
<u>L2</u>	L1 and support\$3	81641	<u>L2</u>
<u>L1</u>	(supress\$3 or disabl\$3 or de near2 activat\$3 or activat\$3 or enabl\$3 or bypass\$3 or skip\$4) near5 (code\$1 or segment\$1 or routine\$1 or function\$1 or program\$1 or instruction\$1)	203557	<u>L1</u>

END OF SEARCH HISTORY

Term	Documents
SKIP\$4	0
SKIP	28700
SKIPPA	9
SKIPABLE	2
SKIPADS	1
SKIPAKI	1
SKIPAL	2
SKIPALIS	2
SKIPANON	1
SKIPANTS	1
SKIPASS	4
(L9 AND (SKIP\$4 OR BYPASS\$3 OR SUPRESS\$6)).USPT,PGPB,JPAB,EPAB,DWPI,TDBD.	30

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L27: Entry 21 of 45

File: USPT

Dec 25, 2001

US-PAT-NO: 6333877

DOCUMENT-IDENTIFIER: US 6333877 B1

TITLE: Static type semiconductor memory device that can suppress standby current

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	KIMC
Draw Desc	Image									

☐ 22. Document ID: US 6298289 B1

L27: Entry 22 of 45

File: USPT

Oct 2, 2001

US-PAT-NO: 6298289

DOCUMENT-IDENTIFIER: US 6298289 B1

**** See image for Certificate of Correction ****

TITLE: Integrated spacecraft control system and method

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	KIMC
Draw Desc	Image									

☐ 23. Document ID: US 6259461 B1

L27: Entry 23 of 45

File: USPT

Jul 10, 2001

US-PAT-NO: 6259461

DOCUMENT-IDENTIFIER: US 6259461 B1

TITLE: System and method for accelerating the rendering of graphics in a multi-pass rendering environment

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	KIMC
Draw Desc	Image									

☐ 24. Document ID: US 6247076 B1

L27: Entry 24 of 45

File: USPT

Jun 12, 2001

US-PAT-NO: 6247076

DOCUMENT-IDENTIFIER: US 6247076 B1

TITLE: Data storing method and apparatus for storing data while effectively utilizing a small capacity of a memory

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
Draw Desc	Image								

KVMC

☐ 25. Document ID: US 6182204 B1

L27: Entry 25 of 45

File: USPT

Jan 30, 2001

US-PAT-NO: 6182204

DOCUMENT-IDENTIFIER: US 6182204 B1

TITLE: PC card capable of providing multiple and/or different card information structures to a personal computer

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
Draw Desc	Image								

KVMC

☐ 26. Document ID: US 6029211 A

L27: Entry 26 of 45

File: USPT

Feb 22, 2000

US-PAT-NO: 6029211

DOCUMENT-IDENTIFIER: US 6029211 A

TITLE: PC card capable multiple functions and corresponding card information structures (CIS) where switch setting element selects CIS to read out based on selection signal

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
Draw Desc	Image								

KVMC

☐ 27. Document ID: US 6005824 A

L27: Entry 27 of 45

File: USPT

Dec 21, 1999

US-PAT-NO: 6005824

DOCUMENT-IDENTIFIER: US 6005824 A

TITLE: Inherently compensated clocking circuit for dynamic random access memory

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
Draw Desc	Image								

KVMC

☐ 28. Document ID: US 5978304 A

L27: Entry 28 of 45

File: USPT

Nov 2, 1999

US-PAT-NO: 5978304

DOCUMENT-IDENTIFIER: US 5978304 A

TITLE: Hierarchical, adaptable-configuration dynamic random access memory

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	KWIC
Draw Desc	Image									

☐ 29. Document ID: US 5970226 A

L27: Entry 29 of 45

File: USPT

Oct 19, 1999

US-PAT-NO: 5970226

DOCUMENT-IDENTIFIER: US 5970226 A

TITLE: Method of non-intrusive testing for a process control interface system having triply redundant remote field units

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	KWIC
Draw Desc	Image									

☐ 30. Document ID: US 5909407 A

L27: Entry 30 of 45

File: USPT

Jun 1, 1999

US-PAT-NO: 5909407

DOCUMENT-IDENTIFIER: US 5909407 A

TITLE: Word line multi-selection circuit for a memory device

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	KWIC
Draw Desc	Image									

☐ 31. Document ID: US 5896331 A

L27: Entry 31 of 45

File: USPT

Apr 20, 1999

US-PAT-NO: 5896331

DOCUMENT-IDENTIFIER: US 5896331 A

TITLE: Reprogrammable addressing process for embedded DRAM

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	KWIC
Draw Desc	Image									

☐ 32. Document ID: US 5848003 A

L27: Entry 32 of 45

File: USPT

Dec 8, 1998

US-PAT-NO: 5848003

DOCUMENT-IDENTIFIER: US 5848003 A

TITLE: Semiconductor memory

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
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KVMC

☐ 33. Document ID: US 5808944 A

L27: Entry 33 of 45

File: USPT

Sep 15, 1998

US-PAT-NO: 5808944

DOCUMENT-IDENTIFIER: US 5808944 A

TITLE: Semiconductor memory device having a defect relief arrangement

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
Draw Desc	Image								

KVMC

☐ 34. Document ID: US 5623448 A

L27: Entry 34 of 45

File: USPT

Apr 22, 1997

US-PAT-NO: 5623448

DOCUMENT-IDENTIFIER: US 5623448 A

TITLE: Apparatus and method for implementing integrated circuit memory device component redundancy using dynamic power distribution switching

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
Draw Desc	Image								

KVMC

☐ 35. Document ID: US 5523974 A

L27: Entry 35 of 45

File: USPT

Jun 4, 1996

US-PAT-NO: 5523974

DOCUMENT-IDENTIFIER: US 5523974 A

**** See image for Certificate of Correction ****

TITLE: Semiconductor memory device with redundant memory cell backup

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
Draw Desc	Image								

KVMC

☐ 36. Document ID: US 5355340 A

L27: Entry 36 of 45

File: USPT

Oct 11, 1994

US-PAT-NO: 5355340

DOCUMENT-IDENTIFIER: US 5355340 A

TITLE: Semiconductor memory with multiplexed redundancy

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
Draw Desc	Image								

K/MC

☐ 37. Document ID: US 5327529 A

L27: Entry 37 of 45

File: USPT

Jul 5, 1994

US-PAT-NO: 5327529

DOCUMENT-IDENTIFIER: US 5327529 A

TITLE: Process of designing user's interfaces for application programs

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
Draw Desc	Image								

K/MC

☐ 38. Document ID: US 5265054 A

L27: Entry 38 of 45

File: USPT

Nov 23, 1993

US-PAT-NO: 5265054

DOCUMENT-IDENTIFIER: US 5265054 A

TITLE: Semiconductor memory with precharged redundancy multiplexing

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
Draw Desc	Image								

K/MC

☐ 39. Document ID: US 5202679 A

L27: Entry 39 of 45

File: USPT

Apr 13, 1993

US-PAT-NO: 5202679

DOCUMENT-IDENTIFIER: US 5202679 A

TITLE: Mid-value signal selection system

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
Draw Desc	Image								

K/MC

☐ 40. Document ID: US 5185736 A

L27: Entry 40 of 45

File: USPT

Feb 9, 1993

US-PAT-NO: 5185736

DOCUMENT-IDENTIFIER: US 5185736 A

TITLE: Synchronous optical transmission system

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
Draw Desc	Image								

K/MC

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(26 NOT 23).USPT,PGPB,JPAB,EPAB,DWPI,TDBD.	45
(L26 NOT L23).USPT,PGPB,JPAB,EPAB,DWPI,TDBD.	45

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(L26 NOT L23).USPT,PGPB,JPAB,EPAB,DWPI,TDBD.	45

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L27

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DATE: Wednesday, December 03, 2003 [Printable Copy](#) [Create Case](#)
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result set

DB=USPT,PGPB,JPAB,EPAB,DWPI,TDBD; PLUR=YES; OP=OR

<u>L27</u>	L26 not l23	45	<u>L27</u>
<u>L26</u>	(supress\$6 or bypass\$3 or skip\$4 or select\$6) near9 (unnecessary or redundan\$4) near6 (application\$1 or software) and (reduc\$3 or sav\$3) near4 (time or power)	46	<u>L26</u>
<u>L25</u>	(supress\$6 or bypass\$3 or skip\$4 or select\$6) near9 (unnecessary or redundan\$4) near6 (application\$1 or software) and (reduc\$3 or sav\$3)	153	<u>L25</u>
<u>L24</u>	(supress\$6 or bypass\$3 or skip\$4 or select\$6) near9 (unnecessary or redundan\$4) near6 (application\$1 or software)	247	<u>L24</u>
<u>L23</u>	(supress\$6 or bypass\$3 or skip\$4) near9 (unnecessary or redundan\$4) near6 (application\$1 or software)	21	<u>L23</u>

<u>L22</u>	l17 and reduc\$3 near4 power	241	<u>L22</u>
<u>L21</u>	l17 and reduc\$3	2641	<u>L21</u>
<u>L20</u>	(supress\$6 or bypass\$3 or skip\$4 or disabl\$3 or de near1 activat\$3) near5 (code\$1 or component\$1 or block\$1) near5 (application or software) near5 select\$6	31	<u>L20</u>
<u>L19</u>	L17 near15 power near4 sav\$3	0	<u>L19</u>
<u>L18</u>	L17 and power near4 sav\$3	63	<u>L18</u>
<u>L17</u>	(supress\$6 or bypass\$3 or skip\$4 or select\$6) near5 (code\$1 or component\$1) near5 (application or software)	4342	<u>L17</u>
<u>L16</u>	l14 and sav\$3 near5 power\$1	0	<u>L16</u>
<u>L15</u>	(supress\$6 or bypass\$3 or skip\$4) near5 (code\$1 or component\$1) near5 (application or software) near6 execut\$4	6	<u>L15</u>
<u>L14</u>	(supress\$6 or bypass\$3 or skip\$4) near5 (code\$1 or component\$1) near5 (application or software)	137	<u>L14</u>
<u>L13</u>	L12 not l10	32	<u>L13</u>
<u>L12</u>	l9 and activat\$3	57	<u>L12</u>
<u>L11</u>	L9 and (skip\$4 or bypass\$3 or supress\$6) and activat\$3	25	<u>L11</u>
<u>L10</u>	L9 and (skip\$4 or bypass\$3 or supress\$6)	30	<u>L10</u>
<u>L9</u>	L8 and mode\$1	81	<u>L9</u>
<u>L8</u>	L7 and (palm\$3 or pager\$1 or personal or handheld or portable)	85	<u>L8</u>
<u>L7</u>	(supress\$3 or disabl\$3 or de near2 activat\$3 or activat\$3 or enabl\$3 or bypass\$3 or skip\$4) near5 (code\$1 or segment\$1 or routine\$1 or function\$1 or program\$1 or instruction\$1) near6 support\$3 near5 application\$1	154	<u>L7</u>
<u>L6</u>	(supress\$3 or disabl\$3 or de near2 activat\$3 or activat\$3 or enabl\$3 or bypass\$3 or skip\$4) near5 (code\$1 or segment\$1 or routine\$1 or function\$1 or program\$1 or instruction\$1) near6 support\$3	2298	<u>L6</u>
<u>L5</u>	L4 and (palm\$3 or pager\$1 or personal or handheld or portable)	226	<u>L5</u>
<u>L4</u>	L1 and support\$3 near5 application and power near4 sav\$3	255	<u>L4</u>
<u>L3</u>	L1 and support\$3 near5 application	7349	<u>L3</u>
<u>L2</u>	L1 and support\$3	81641	<u>L2</u>
<u>L1</u>	(supress\$3 or disabl\$3 or de near2 activat\$3 or activat\$3 or enabl\$3 or bypass\$3 or skip\$4) near5 (code\$1 or segment\$1 or routine\$1 or function\$1 or program\$1 or instruction\$1)	203557	<u>L1</u>

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(L28 NOT L26).USPT.	26

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L29

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result set

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<u>L29</u>	L28 not l26	26	<u>L29</u>
<u>L28</u>	(supress\$3 or inhibit\$4 or stop\$4 or bypass\$3) near9 (field41 or bit\$1) near9 (next or subsequent\$3 or second or succeed\$3) near9 (execut\$3 or fetch\$3)	56	<u>L28</u>
<u>L27</u>	L26 not l25	13	<u>L27</u>
<u>L26</u>	(supress\$3 or inhibit\$4 or stop\$4 or bypass\$3) near7 (field41 or bit\$1) near7 (next or subsequent\$3 or second or succeed\$3) near9 (execut\$3 or fetch\$3)	30	<u>L26</u>
<u>L25</u>	(supress\$3 or inhibit\$4 or stop\$4 or bypass\$3) near5 (field41 or bit\$1) near5 (next or subsequent\$3 or second or succeed\$3) near8 (execut\$3 or fetch\$3)	17	<u>L25</u>

<u>L24</u>	4937783.pn.	1	<u>L24</u>
<u>L23</u>	L20 and (format or field\$1 or bit\$1 or extension or extend\$3) near6 instruction and (bypass\$3 or skip\$4 or jump\$3 or branch\$3 or link\$3) near5 (next or subsequent\$3 or another or succeed\$3)	145	<u>L23</u>
<u>L22</u>	L20 and (format or field\$1 or bit\$1 or extension or extend\$3) near6 instruction and (bypass\$3 or skip\$4 or jump\$3 or branch\$3 or link\$3)	298	<u>L22</u>
<u>L21</u>	L20 and (format or field\$1 or bit\$1 or extension or extend\$3) near6 instruction	336	<u>L21</u>
<u>L20</u>	l18 or L19	581	<u>L20</u>
<u>L19</u>	((712/245)!.CCLS.)	436	<u>L19</u>
<u>L18</u>	l16 or L17	147	<u>L18</u>
<u>L17</u>	((717/156)!.CCLS.)	97	<u>L17</u>
<u>L16</u>	((717/155)!.CCLS.)	59	<u>L16</u>
<u>L15</u>	((717/)!.CCLS.)	0	<u>L15</u>
<u>L14</u>	(field\$1 or bit\$1) near8 (next or susequent\$3 or preceed\$3) near7 execut\$3 near15 (redirect\$4 or flow or sequnc\$3)	10	<u>L14</u>
<u>L13</u>	(field\$1 or bit\$1) near8 (next or susequent\$3 or preceed\$3) near7 execut\$3 and (redirect\$4 or flow or sequnc\$3)	1035	<u>L13</u>
<u>L12</u>	(field\$1 or bit\$1) near8 (jump\$5 or branch\$3) near7 execut\$3 near10 (redirect\$4 or flow or sequnc\$3)	10	<u>L12</u>
<u>L11</u>	(field\$1 or bit\$1) near8 (jump\$5 or branch\$3) near10 (redirect\$4 or flow or sequnc\$3)	338	<u>L11</u>
<u>L10</u>	6272620.pn. and state	1	<u>L10</u>
<u>L9</u>	L7 near9 computer near9 conventional	35	<u>L9</u>
<u>L8</u>	L7 near9 computer	167	<u>L8</u>
<u>L7</u>	non near3 von neuman	3767	<u>L7</u>
<u>L6</u>	L5 near9 computer	124	<u>L6</u>
<u>L5</u>	non near3 van neuman	3792	<u>L5</u>
<u>L4</u>	L3 and computer	116	<u>L4</u>
<u>L3</u>	L1 near7 conventional	710	<u>L3</u>
<u>L2</u>	L1 and conventional	102054	<u>L2</u>
<u>L1</u>	van neuman	189199	<u>L1</u>

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L11: Entry 1 of 49

File: USPT

Dec 2, 2003

US-PAT-NO: 6658561

DOCUMENT-IDENTIFIER: US 6658561 B1

TITLE: Hardware device for executing programmable instructions based upon micro-instructions

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC
Draw Desc	Image										

☐ 2. Document ID: US 6651164 B1

L11: Entry 2 of 49

File: USPT

Nov 18, 2003

US-PAT-NO: 6651164

DOCUMENT-IDENTIFIER: US 6651164 B1

TITLE: System and method for detecting an erroneous data hazard between instructions of an instruction group and resulting from a compiler grouping error

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC
Draw Desc	Image										

☐ 3. Document ID: US 6646944 B2

L11: Entry 3 of 49

File: USPT

Nov 11, 2003

US-PAT-NO: 6646944

DOCUMENT-IDENTIFIER: US 6646944 B2

TITLE: Semiconductor memory device

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KWIC
Draw Desc	Image										

☐ 4. Document ID: US 6625740 B1

L11: Entry 4 of 49

File: USPT

Sep 23, 2003

US-PAT-NO: 6625740

DOCUMENT-IDENTIFIER: US 6625740 B1

TITLE: Dynamically activating and deactivating selected circuit blocks of a data processing integrated circuit during execution of instructions according to power code bits appended to selected instructions

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KMIC
Draw Desc	Image										

☐ 5. Document ID: US 6611934 B2

L11: Entry 5 of 49

File: USPT

Aug 26, 2003

US-PAT-NO: 6611934

DOCUMENT-IDENTIFIER: US 6611934 B2

TITLE: Boundary scan test cell circuit

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	Claims	KMIC
Draw Desc	Image										

☐ 6. Document ID: US 6594802 B1

L11: Entry 6 of 49

File: USPT

Jul 15, 2003

US-PAT-NO: 6594802

DOCUMENT-IDENTIFIER: US 6594802 B1

TITLE: Method and apparatus for providing optimized access to circuits for debug, programming, and test

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	KMIC
Draw Desc	Image									

☐ 7. Document ID: US 6553527 B1

L11: Entry 7 of 49

File: USPT

Apr 22, 2003

US-PAT-NO: 6553527

DOCUMENT-IDENTIFIER: US 6553527 B1

TITLE: Programmable array built-in self test method and controller with programmable expect generator

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	KMIC
Draw Desc	Image									

☐ 8. Document ID: US 6553526 B1

L11: Entry 8 of 49

File: USPT

Apr 22, 2003

US-PAT-NO: 6553526

DOCUMENT-IDENTIFIER: US 6553526 B1

TITLE: Programmable array built-in self test method and system for arrays with imbedded logic

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	KWIC
Draw Desc	Image									

☐ 9. Document ID: US 6551846 B1

L11: Entry 9 of 49

File: USPT

Apr 22, 2003

US-PAT-NO: 6551846

DOCUMENT-IDENTIFIER: US 6551846 B1

TITLE: Semiconductor memory device capable of correctly and surely effecting voltage stress acceleration

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	KWIC
Draw Desc	Image									

☐ 10. Document ID: US 6463520 B1

L11: Entry 10 of 49

File: USPT

Oct 8, 2002

US-PAT-NO: 6463520

DOCUMENT-IDENTIFIER: US 6463520 B1

TITLE: Processor for executing instruction codes of two different lengths and device for inputting the instruction codes

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	KWIC
Draw Desc	Image									

☐ 11. Document ID: US 6438671 B1

L11: Entry 11 of 49

File: USPT

Aug 20, 2002

US-PAT-NO: 6438671

DOCUMENT-IDENTIFIER: US 6438671 B1

TITLE: Generating partition corresponding real address in partitioned mode supporting system

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	KWIC
Draw Desc	Image									

☐ 12. Document ID: US 6425122 B1

L11: Entry 12 of 49

File: USPT

Jul 23, 2002

US-PAT-NO: 6425122

DOCUMENT-IDENTIFIER: US 6425122 B1

TITLE: Single stepping system and method for tightly coupled processors

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	KWIC
Draw Desc	Image									

☐ 13. Document ID: US 6304987 B1

L11: Entry 13 of 49

File: USPT

Oct 16, 2001

US-PAT-NO: 6304987

DOCUMENT-IDENTIFIER: US 6304987 B1

**** See image for Certificate of Correction ****

TITLE: Integrated test circuit

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	KWIC
Draw Desc	Image									

☐ 14. Document ID: US 6209079 B1

L11: Entry 14 of 49

File: USPT

Mar 27, 2001

US-PAT-NO: 6209079

DOCUMENT-IDENTIFIER: US 6209079 B1

TITLE: Processor for executing instruction codes of two different lengths and device for inputting the instruction codes

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	KWIC
Draw Desc	Image									

☐ 15. Document ID: US 6208772 B1

L11: Entry 15 of 49

File: USPT

Mar 27, 2001

US-PAT-NO: 6208772

DOCUMENT-IDENTIFIER: US 6208772 B1

TITLE: Data processing system for logically adjacent data samples such as image data in a machine vision system

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	KWIC
Draw Desc	Image									

☐ 16. Document ID: US 6161166 A

L11: Entry 16 of 49

File: USPT

Dec 12, 2000

US-PAT-NO: 6161166

DOCUMENT-IDENTIFIER: US 6161166 A

TITLE: Instruction cache for multithreaded processor

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
Draw Desc	Image								

[KWC](#)

☐ 17. Document ID: US 6081916 A

L11: Entry 17 of 49

File: USPT

Jun 27, 2000

US-PAT-NO: 6081916

DOCUMENT-IDENTIFIER: US 6081916 A

TITLE: IC with test cells having separate data and test paths

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
Draw Desc	Image								

[KWC](#)

☐ 18. Document ID: US 5960212 A

L11: Entry 18 of 49

File: USPT

Sep 28, 1999

US-PAT-NO: 5960212

DOCUMENT-IDENTIFIER: US 5960212 A

TITLE: Universal input/output controller having a unique coprocessor architecture

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
Draw Desc	Image								

[KWC](#)

☐ 19. Document ID: US 5951674 A

L11: Entry 19 of 49

File: USPT

Sep 14, 1999

US-PAT-NO: 5951674

DOCUMENT-IDENTIFIER: US 5951674 A

TITLE: Object-code compatible representation of very long instruction word programs

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
Draw Desc	Image								

[KWC](#)

☐ 20. Document ID: US 5933651 A

L11: Entry 20 of 49

File: USPT

Aug 3, 1999

US-PAT-NO: 5933651

DOCUMENT-IDENTIFIER: US 5933651 A

TITLE: Programmable controller

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
Draw Desc	Image								

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Term	Documents
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USED\$S	16
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UNUSED\$S	0
FIELD	82
FIELD\$S	8
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BYPASS	81034
BYPASS\$A	1
BYPASS\$AGE	31
BYPASS\$CMD	7
((BYPASS\$3 OR SUPRESS\$3 OR INHIBIT\$3 OR STOP\$3 OR SUSPEND\$3 OR SUSPENSION\$1 OR ACTIVAT\$3 OR DEACTIVAT\$3 OR DISABL\$3) NEAR5 (CODE\$1 OR SEQUENC\$3 OR PROGRAM\$1 OR BLOCK\$1 OR SEGMENT\$1 OR LOOP\$1 OR ROUTINE\$1 OR INSTRUCTION\$1 OR PORTION\$1) NEAR8 (USED OR UNUSED OR MINIMIZ\$3 OR REDUC\$3) NEAR15 (FIELD OR BIT\$1 OR EXTEND\$3 OR EXTENSION\$1) NEAR4 INSTRUCTION\$1).USPT.	49

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WEST[Generate Collection](#)[Print](#)**Search Results - Record(s) 21 through 40 of 49 returned.**☐ 21. Document ID: US 5847978 A

L11: Entry 21 of 49

File: USPT

Dec 8, 1998

US-PAT-NO: 5847978

DOCUMENT-IDENTIFIER: US 5847978 A

TITLE: Processor and control method for performing proper saturation operation

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
Draw Desc	Image								

[KMC](#)☐ 22. Document ID: US 5710913 A

L11: Entry 22 of 49

File: USPT

Jan 20, 1998

US-PAT-NO: 5710913

DOCUMENT-IDENTIFIER: US 5710913 A

TITLE: Method and apparatus for executing nested loops in a digital signal processor

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
Draw Desc	Image								

[KMC](#)☐ 23. Document ID: US 5631911 A

L11: Entry 23 of 49

File: USPT

May 20, 1997

US-PAT-NO: 5631911

DOCUMENT-IDENTIFIER: US 5631911 A

TITLE: Integrated test circuit

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
Draw Desc	Image								

[KMC](#)☐ 24. Document ID: US 5602855 A

L11: Entry 24 of 49

File: USPT

Feb 11, 1997

US-PAT-NO: 5602855

DOCUMENT-IDENTIFIER: US 5602855 A

TITLE: Integrated test circuit

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	KWIC
Draw Desc	Image									

☐ 25. Document ID: US 5594917 A

L11: Entry 25 of 49

File: USPT

Jan 14, 1997

US-PAT-NO: 5594917

DOCUMENT-IDENTIFIER: US 5594917 A

**** See image for Certificate of Correction ****

TITLE: High speed programmable logic controller

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	KWIC
Draw Desc	Image									

☐ 26. Document ID: US 5570375 A

L11: Entry 26 of 49

File: USPT

Oct 29, 1996

US-PAT-NO: 5570375

DOCUMENT-IDENTIFIER: US 5570375 A

TITLE: IEEE Std. 1149.1 boundary scan circuit capable of built-in self-testing

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	KWIC
Draw Desc	Image									

☐ 27. Document ID: US 5517436 A

L11: Entry 27 of 49

File: USPT

May 14, 1996

US-PAT-NO: 5517436

DOCUMENT-IDENTIFIER: US 5517436 A

TITLE: Digital signal processor for audio applications

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	KWIC
Draw Desc	Image									

☐ 28. Document ID: US 5504930 A

L11: Entry 28 of 49

File: USPT

Apr 2, 1996

US-PAT-NO: 5504930

DOCUMENT-IDENTIFIER: US 5504930 A

TITLE: Programmable controller and sequence control method

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
Draw Desc	Image								

KVMC

☐ 29. Document ID: US 5502683 A

L11: Entry 29 of 49

File: USPT

Mar 26, 1996

US-PAT-NO: 5502683

DOCUMENT-IDENTIFIER: US 5502683 A

TITLE: Dual ported memory with word line access control

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
Draw Desc	Image								

KVMC

☐ 30. Document ID: US 5495487 A

L11: Entry 30 of 49

File: USPT

Feb 27, 1996

US-PAT-NO: 5495487

DOCUMENT-IDENTIFIER: US 5495487 A

TITLE: Testing buffer/register

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
Draw Desc	Image								

KVMC

☐ 31. Document ID: US 5428754 A

L11: Entry 31 of 49

File: USPT

Jun 27, 1995

US-PAT-NO: 5428754

DOCUMENT-IDENTIFIER: US 5428754 A

TITLE: Computer system with clock shared between processors executing separate instruction streams

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
Draw Desc	Image								

KVMC

☐ 32. Document ID: US 5423014 A

L11: Entry 32 of 49

File: USPT

Jun 6, 1995

US-PAT-NO: 5423014

DOCUMENT-IDENTIFIER: US 5423014 A

TITLE: Instruction fetch unit with early instruction fetch mechanism

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments
Draw Desc	Image								

KVMC

☐ 37. Document ID: US 5073855 A

L11: Entry 37 of 49

File: USPT

Dec 17, 1991

US-PAT-NO: 5073855

DOCUMENT-IDENTIFIER: US 5073855 A

TITLE: Resource conflict detection method and apparatus included in a pipelined processing unit

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	KWIC
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☐ 38. Document ID: US 5056094 A

L11: Entry 38 of 49

File: USPT

Oct 8, 1991

US-PAT-NO: 5056094

DOCUMENT-IDENTIFIER: US 5056094 A

TITLE: Delay fault testing method and apparatus

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	KWIC
Draw Desc	Image									

☐ 39. Document ID: US 5056015 A

L11: Entry 39 of 49

File: USPT

Oct 8, 1991

US-PAT-NO: 5056015

DOCUMENT-IDENTIFIER: US 5056015 A

TITLE: Architectures for serial or parallel loading of writable control store

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	KWIC
Draw Desc	Image									

☐ 40. Document ID: US 5042000 A

L11: Entry 40 of 49

File: USPT

Aug 20, 1991

US-PAT-NO: 5042000

DOCUMENT-IDENTIFIER: US 5042000 A

TITLE: Integral transform method

Full	Title	Citation	Front	Review	Classification	Date	Reference	Sequences	Attachments	KWIC
Draw Desc	Image									

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USED\$S	16
UNUSED	52356
UNUSED\$S	0
FIELD	82
FIELD\$S	8
BYPASS\$3	0
BYPASS	81034
BYPASSA	1
BYPASSAGE	31
BYPASSCMD	7
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<u>L9</u>	L7 near15 (field41 or bit\$1 or flag\$1 or extension or extend\$3) near5 instruction\$1 and (reduc\$3 or minimiz\$3 or unused)	1155	<u>L9</u>
<u>L8</u>	L7 near15 (field41 or bit\$1 or flag\$1 or extension or extend\$3) near5 instruction\$1	1360	<u>L8</u>
<u>L7</u>	(bypass\$3 or inhibit\$3 or stop\$3 or suspend\$3 or suspension\$1 or activat\$3 or deactivat\$3 or disabl\$3) near5 (code\$1 or sequenc\$3 or program\$1 or block\$1 or segment\$1 or loop\$1 or routine\$1 or instruction\$1 or portion\$1)	243249	<u>L7</u>
<u>L6</u>	L4 or L5	11051	<u>L6</u>
<u>L5</u>	((714/3 714/4 714/5 714/6 714/7 714/8 714/9 714/10 714/11 714/12)!.CCLS.)	3219	<u>L5</u>
<u>L4</u>	L1 or L2 or L3	7874	<u>L4</u>
<u>L3</u>	((455/445)!.CCLS.)	666	<u>L3</u>
<u>L2</u>	((717/121 717/122 717/123 717/124 717/125 717/126 717/127 717/128 717/129 717/130 717/131 717/132 717/133 717/134 717/135 717/136 717/137 717/138 717/139 717/140 717/141 717/142 717/143 717/144 717/145 717/146 717/147 717/148 717/149 717/150 717/151 717/152 717/153 717/154 717/155 717/156)!.CCLS.)	2306	<u>L2</u>
<u>L1</u>	((712/207 712/208 712/209 712/210 712/211 712/212 712/213 712/214 712/215 712/216 712/217 712/218 712/219 712/220 712/221 712/222 712/223 712/224 712/225 712/226 712/227 712/228 712/229 712/230 712/231 712/232 712/233 712/234 712/235 712/236 712/237 712/238 712/239 712/240 712/241 712/242 712/243 712/244 712/245)!.CCLS.)	5092	<u>L1</u>

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